

PRODUCT SUMMARY

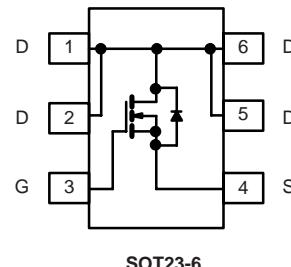
- $V_{DS(V)} = 30V$
- $I_D = 6 A$
- $R_{DS(ON)} < 38m\Omega$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 50m\Omega$ ($V_{GS} = 4.5V$)

FEATURES

- Low On-Resistance

APPLICATIONS

- DC/DC Converters, High Speed Switching

**ABSOLUTE MAXIMUM RATINGS** ($T_A = 25^\circ C$, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150^\circ C$)	I_D	6 ^e	A
		6 ^e	
		5.5 ^{b, c}	
		4.4 ^{b, c}	
Pulsed Drain Current ($t = 300 \mu s$)	I_{DM}	25	
Continuous Source-Drain Diode Current	I_S	2.1	
		1.1 ^{b, c}	
Maximum Power Dissipation	P_D	2.5	W
		1.6	
		1.3 ^{b, c}	
		0.8 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150	°C
Soldering Recommendations (Peak Temperature)		260	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	$t \leq 5 s$	R_{thJA}	75	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	40	
			50	

Notes:

- a. Based on $T_C = 25^\circ C$.
- b. Surface mounted on 1" x 1" FR4 board.
- c. $t = 5 s$.
- d. Maximum under steady state conditions is 166 °C/W.
- e. Package limited.

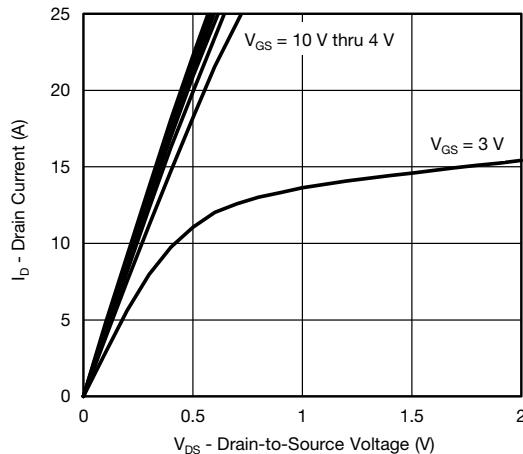
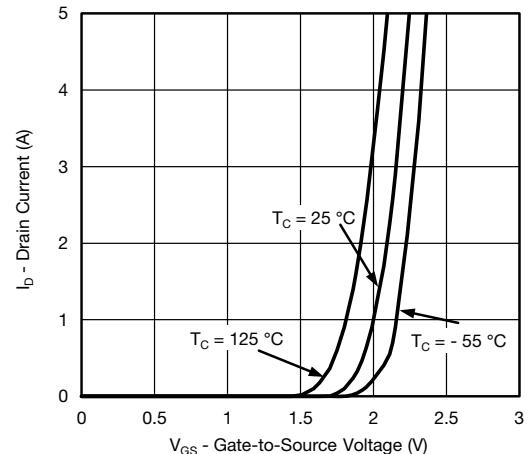
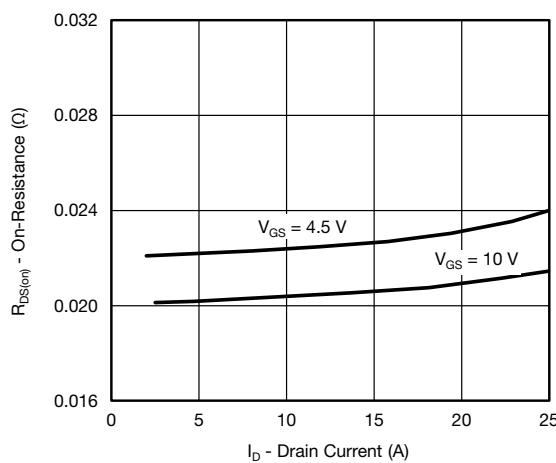
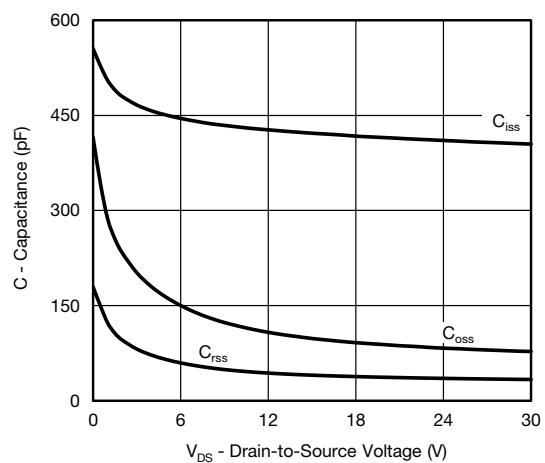
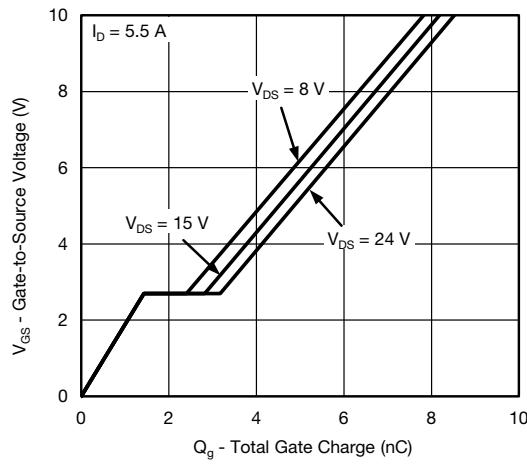
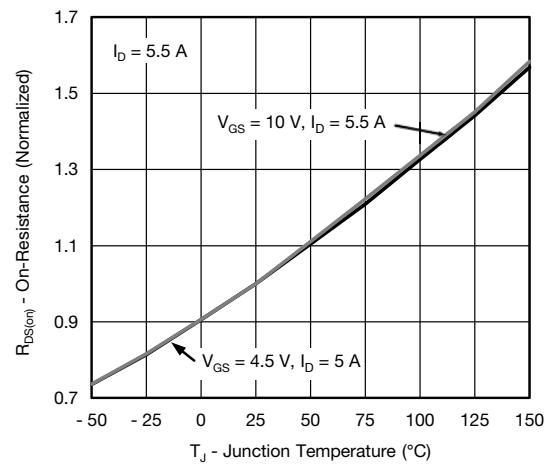
SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)

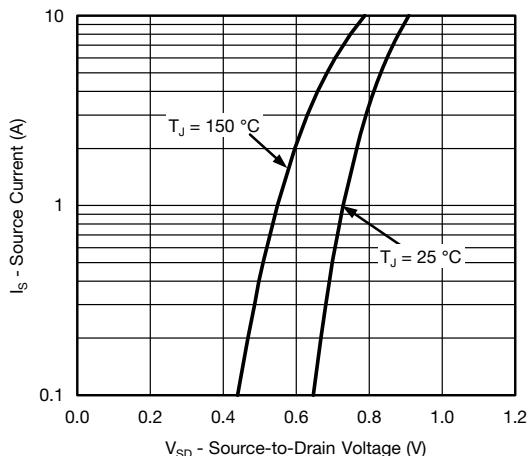
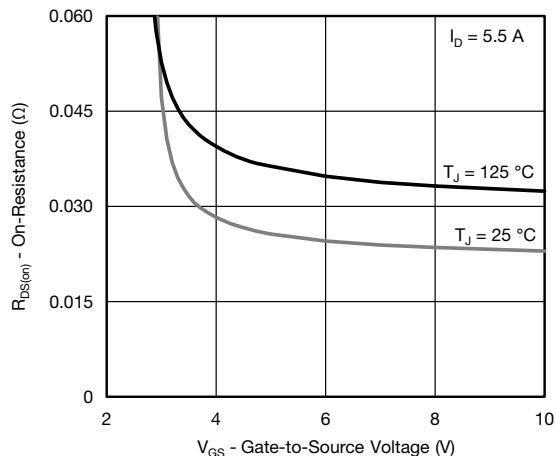
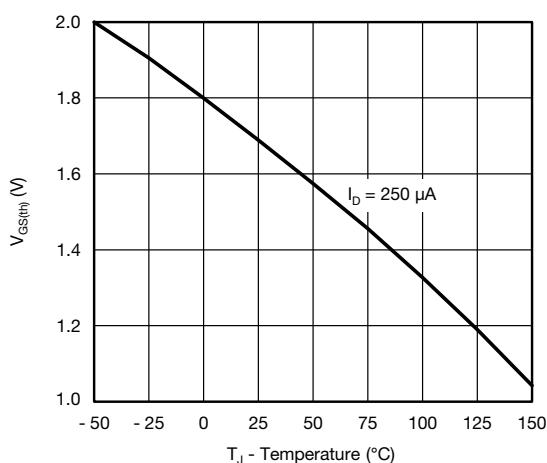
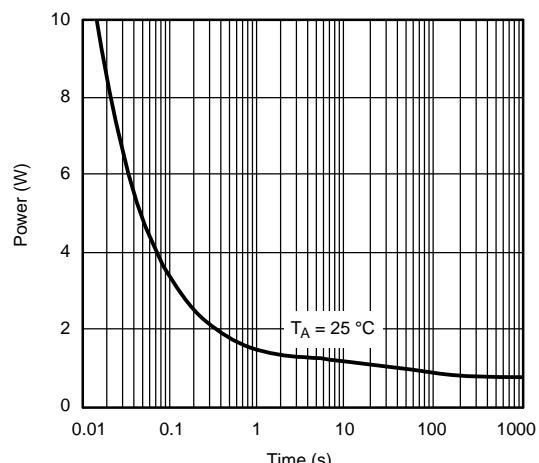
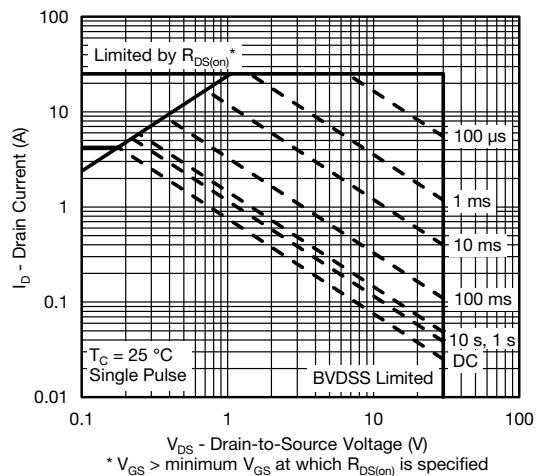
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA	30			mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 4.8		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.7		1.5	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V		1		μA
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 70 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≤ 5 V, V _{GS} = 10 V	20			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 5.5 A		26	30	mΩ
		V _{GS} = 4.5 V, I _D = 5 A		28	40	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 5.5 A		24		S
Dynamic^b						
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		424		pF
Output Capacitance	C _{oss}			100		
Reverse Transfer Capacitance	C _{rss}			42		
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 5.5 A		8.2	13	nC
				4.2	7	
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 5.5 A		1.4		
Gate-Drain Charge	Q _{gd}			1.4		
Gate Resistance	R _g	f = 1 MHz	2.5	12.6	25.2	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 3.4 Ω I _D ≈ 4.4 A, V _{GEN} = 4.5 V, R _g = 1 Ω		6	12	ns
Rise Time	t _r			20	30	
Turn-Off Delay Time	t _{d(off)}			14	21	
Fall Time	t _f			10	20	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 3.4 Ω I _D ≈ 4.4 A, V _{GEN} = 10 V, R _g = 1 Ω		3	6	
Rise Time	t _r			11	20	
Turn-Off Delay Time	t _{d(off)}			20	30	
Fall Time	t _f			7	14	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			2.1	A
Pulse Diode Forward Current	I _{SM}				25	
Body Diode Voltage	V _{SD}	I _S = 4.4 A, V _{GS} = 0 V		0.82	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 4.4 A, dI/dt = 100 A/μs, T _J = 25 °C		13	20	ns
Body Diode Reverse Recovery Charge	Q _{rr}			6	12	
Reverse Recovery Fall Time	t _a			8		ns
Reverse Recovery Rise Time	t _b			5		

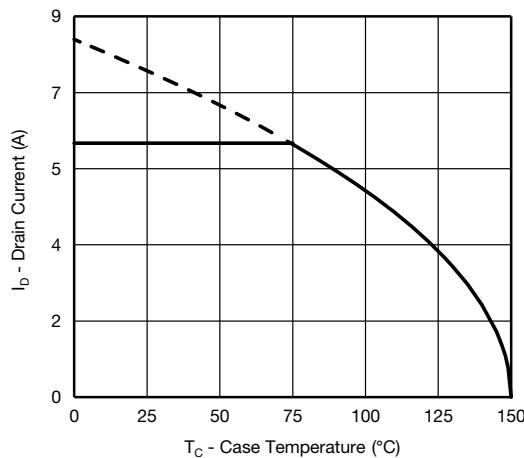
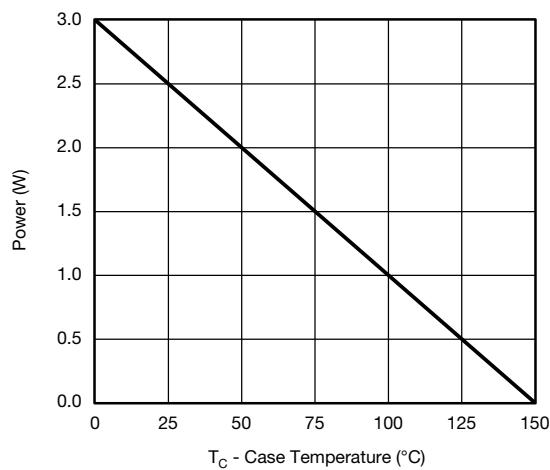
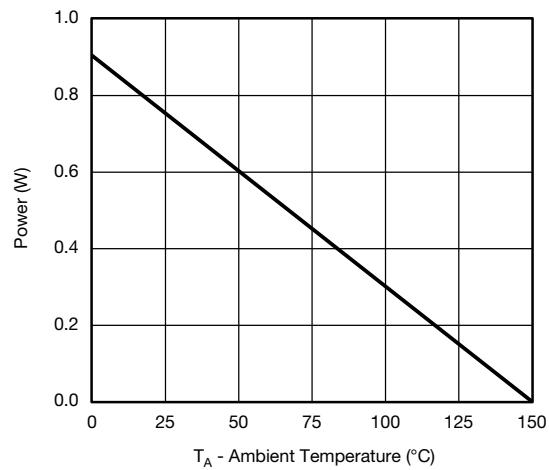
Notes:

a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.

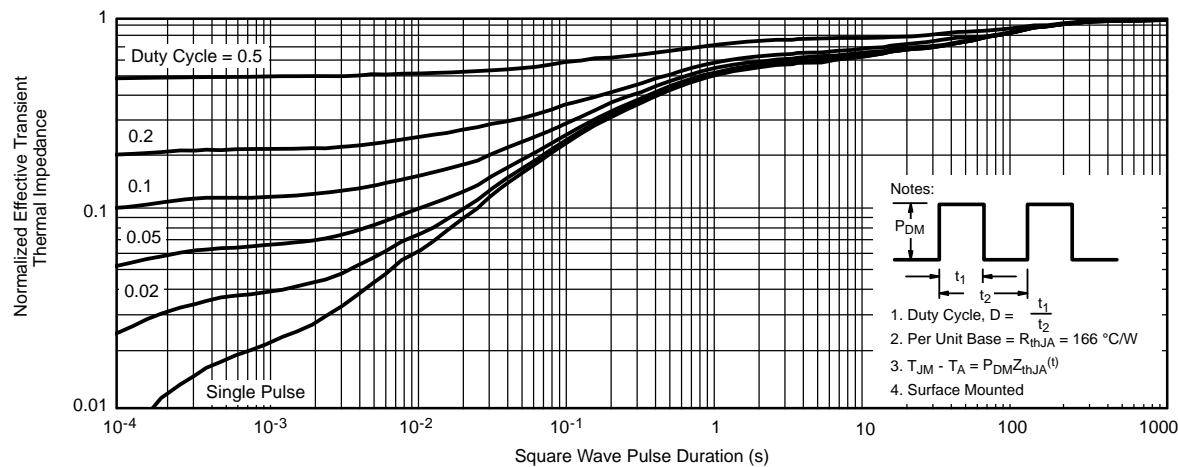
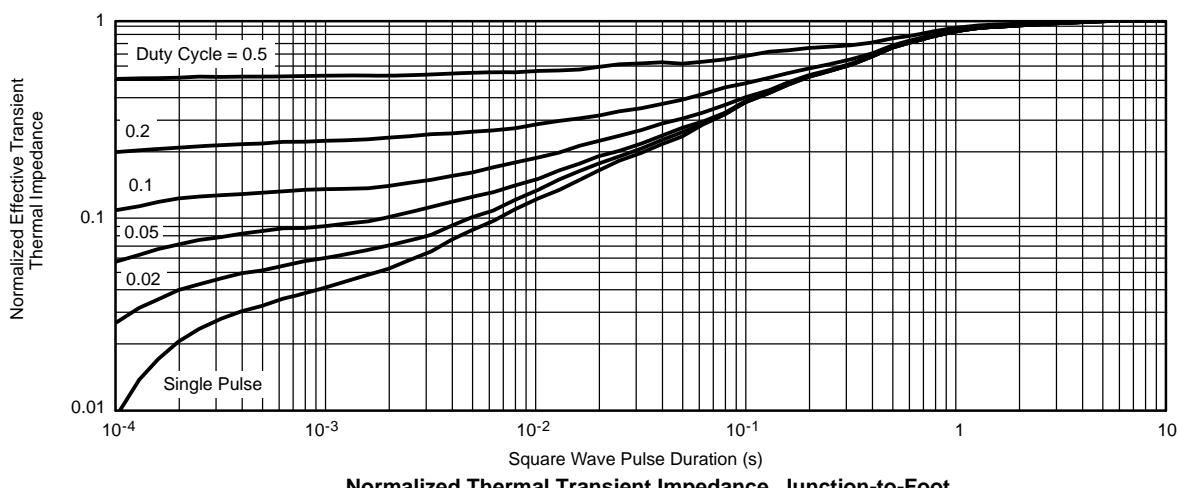
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**Output Characteristics****Transfer Characteristics****On-Resistance vs. Drain Current and Gate Voltage****Capacitance****Gate Charge****On-Resistance vs. Junction Temperature**

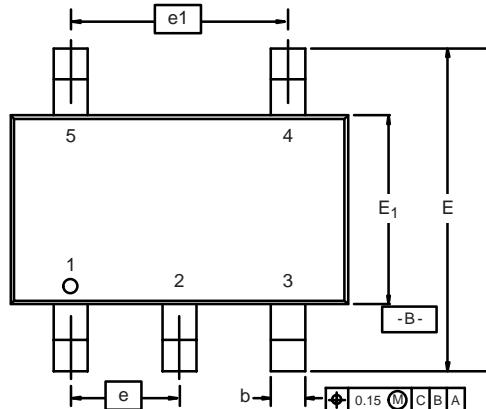
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power (Junction-to-Ambient)****Safe Operating Area, Junction-to-Ambient**

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**Current Derating*****Power Derating, Junction-to-Foot****Power Derating, Junction-to-Ambient**

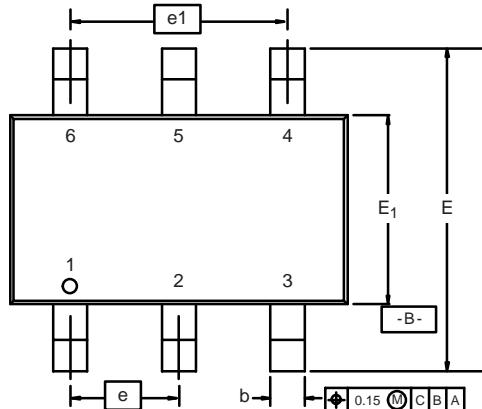
* The power dissipation P_D is based on $T_{J(\max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**Normalized Thermal Transient Impedance, Junction-to-Ambient****Normalized Thermal Transient Impedance, Junction-to-Foot**

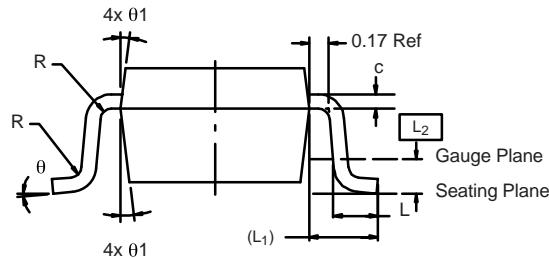
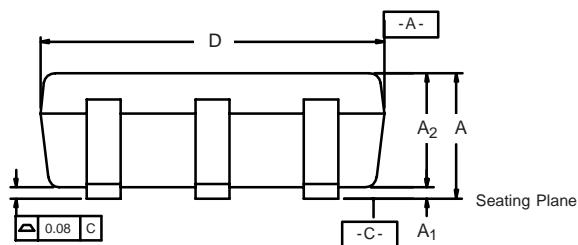
SOT23-5/6 PACKAGE OUTLINE DIMENSIONS



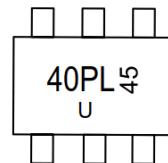
SOT23-5



SOT23-6



Dim	MILLIMETERS			INCHES								
	Min	Nom	Max	Min	Nom	Max						
A	0.91	-	1.10	0.036	-	0.043						
A₁	0.01	-	0.10	0.0004	-	0.004						
A₂	0.90	-	1.00	0.035	0.038	0.039						
b	0.30	0.32	0.45	0.012	0.013	0.018						
c	0.10	0.15	0.20	0.004	0.006	0.008						
D	2.95	3.05	3.10	0.116	0.120	0.122						
E	2.70	2.85	2.98	0.106	0.112	0.117						
E₁	1.55	1.65	1.70	0.061	0.065	0.067						
e	0.95 BSC			0.0374 BSC								
e₁	1.80	1.90	2.00	0.071	0.075	0.079						
L	0.32	-	0.50	0.012	-	0.020						
L₁	0.60 Ref			0.024 Ref								
L₂	0.25 BSC			0.010 BSC								
R	0.10	-	-	0.004	-	-						
θ	0°	4°	8°	0°	4°	8°						
θ₁	7° Nom			7° Nom								
ECN: C-06593-Rev. I, 18-Dec-06												
DWG: 5540												

Marking**Ordering information**

Order code	Package	Baseqty	Deliverymode
AO6400	SOT23-6	3000	Tape and reel